

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	IS&R	L11	393	(326/80).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/21 21:12			0
2	BRS	L12	94	11 and latch\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/21 21:36			0
3	BRS	L13	10	han-gyo\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/21 21:38			0
4	BRS	L14	47	(buffer driver convert\$ shifter) and ((fenc\$4 and latch\$3) same circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/21 21:42			0
5	BRS	L15	12	fencing adj2 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/21 21:42			0